## **IL2931 SERIES**

## LOW DROPOUT VOLTAGE REGULATORS

The LM2931 series consists of positive fixed and adjust-

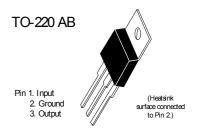
able output voltage regulators that are specifically designed to maintain proper regulation with an extremely low input-to-output voltage differential. These devices are capable of supplying output currents in excess of 100 mA and feature a low bias current of 0.4 mA at 10 mA output.

Designed primarily to survive in the harsh automotive environment, these devices will protect all external load circuitry from battery jump starts, and excessive line transients during load dump. This series also includes internal current limiting, thermal shutdown, and additionally, is able to withstand temporary power-up with mirror-image insertion.

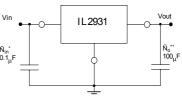
Due to the low dropout voltage and bias current specifications, the LM2931 series is ideally suited for battery powered industrial and consumer equipment where an extension of useful battery life is desirable. The 'C' suffix adjustable output regulators feature an output inhibit pin which is extremely useful in microprocessor-based systems.

- Input-to-Output Voltage Differential of Less Then 0.6 V at 100mA
- Output Current in Excess of 100 mA
- Low Bias Current
- 60 V Load Dump Protection
- -50 V Reverse Transient Protection
- Internal Current Limiting with Thermal Shutdown
- Temporary Mirror-Image Protection
- Ideally Suited for Battery Powered Equipment

# TO-226AA / TO-92 Pin 1. Output 2. Ground 3. Input



### Standard application



## **Ordering Information**

	Output		Package Case		
Device	Voltage	Tolerance	Number		
II2931-5	5.0 V	+5%	TO-226AA/TO92, TO-220AB		
II2931-9	9.0 V	+5%	TO-226AA/TO92, TO-220AB		

### Maximum Ratings

Rating	Symbol	Value	Unit
Input Voltage Continuos	Vin	40	Vdc
Transient Input Voltage (r ≤ 100 ms)	Vin(r)	60	Vpk
Transient Reverse Polarity Input Voltage	-Vin(r)	-50	Vpk
1,0% Duty Cycle, r ≤ 100 ms			
Power Dissipation Case TO-220AA (TO-92)			
T <b>A = +</b> 25 °C	PD	Internally Limited	Watts
Thermal Resistance Junction to Ambient	R <sub>⊕JA</sub>	178	°C/W
Thermal Resistance Junction to Case	R <sub>⊕JC</sub>	83	°C/W
Power Dissipation Case TO-220AB			
T <b>A = +</b> 25 °C	PD	Internally Limited	Watts
Thermal Resistance Junction to Ambient	$R_{\Theta JC}$	65	°C/W
Thermal Resistance Junction to Case	R <sub>⊕JC</sub>	5.0	°C/W
Tested Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	Tstg	-65 to +150	С



# **IL2931 SERIES**

### IL2931-5

### **Electrical characteristics**

(Vin = 14V, Io = 10mA, Co = 100  $\mu$ F, Co(ESR) = 0.3  $\Omega$ , TJ = +25 °C Note 1, unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Fixed Output					
Output Voltage	Vo				Vcd
$V_{in} = 14 \text{ V}, \text{ Io} = 10 \text{ mA}, T_J = 25 ^{\circ}\text{C}$		4.75	5.0	5.25	
$V_{in}$ = 6.0 to 26 V, $I_o \le 100$ mA, $T_J$ = -40 to 125°C		4.5	-	5.5	
Line Regulation	Reg <sub>line</sub>				mV
$V_{in}$ = 9.0 V to 16 V		-	2.0	10	
$V_{in}$ = 6.0 V to 26 V		-	4.0	30	
Load Regulation(Io = 5.0 mA to 100 m A)	Reg <sub>load</sub>	-	14	50	mV
Output Impedance	Zo	-	200	-	mΩ
$I_0 = 10 \text{ mA}, \Delta I_0 = 1.0 \text{ mA}, f = 100 \text{ Hz to } 10 \text{ kHz}$					
Bias Current	I <sub>B</sub>				mA
$V_{in} = 14 \text{ V}, I_o = 100 \text{ mA}, TJ = 25 ^{\circ}\text{C}$		-	5.8	30	
$V_{in} = 6.0 \text{ V to } 26 \text{ V}, I_{o} = 10 \text{ mA}, T_{J} = -40 \text{ to } +125$		-	0.4	1.0	
°C					
Output Noise Voltage (f = 10 Hz to 100 rHzZ)	$V_n$	-	700	-	μVrms
Long -Term Stability	S	-	20	-	mV/kHz
Ripple Rejection (f = 120 Hz)	RR	60	90	-	dB
Dropout Voltage	V <sub>in</sub> -V <sub>o</sub>				V
Io=10A		-	0.015	0.2	
lo=100A			0.06	0.6	
Over -Voltage Shutdown Threshold	$V_{th(ov)}$	26	29.5	40	V
Output Voltage with Reverse Polarity Input	-V <sub>o</sub>	-0.3	0	-	V
(Vin = -15 V)					

### IL2931-9

### **Electrical characteristics**

(Vin = 16V, Io = 10 mA)

Characteristic	Symbol	Min	Тур	Max	Unit
Fixed Output					
Output Voltage	Vo				Vcd
$V_{in} = 16 \text{ V}, I_o = 10 \text{ mA}, TJ = 25 ^{\circ}\text{C}$		8.55	9.0	9.45	
$V_{in}$ =10 to 26 V, $I_o \le 100 \text{mA}$ , $T_J = -40 \text{ to } +125 ^{\circ}\text{C}$		8.1	-	9.9	
Line Regulation	Regline				mV
$V_{in} = 9.0 \text{ V to } 16 \text{ V}$		-	4.0	20	
V <sub>in</sub> = 6.0 V to 26 V		-	8.0	60	
Load Regulation(Io = 5.0 mA to 100 m A)	Reg <sub>load</sub>	-	30	100	mV
Output Impedance	Z <sub>o</sub>	-	200	-	$m\Omega$
$I_0 = 10 \text{ mA}, \Delta I_0 = 1.0 \text{ mA}, f = 100 \text{ Hz to } 10 \text{ kHz}$					
Bias Current	lΒ				mA
$V_{in} = 14 \text{ V}, I_0 = 100 \text{ mA}, T_J = 25 ^{\circ}\text{C}$		-	5.8	30	
$V_{in} = 6.0 \text{ V to } 26 \text{ V}, I_{o} = 10 \text{ mA}, T_{J} = -40 \text{ to } +125$		-	0.4	1.0	
°C					
Output Noise Voltage (f = 10 Hz to 100 rHzZ)	$V_n$	-	700	-	μVrms
Long -Term Stability	S	-	20	-	mV/kHz
Ripple Rejection (f = 120 Hz)	RR	60	90	-	dB
Dropout Voltage	$V_{in}$ - $V_{o}$				V
lo=10A		-	0.015	0.2	
Io=100A			0.06	0.6	
Over - Voltage Shutdown Threshold	$V_{th(ov)}$	26	29.5	40	V
Output Voltage with Reverse Polarity Input (V <sub>in</sub> = -15 V)	-V <sub>o</sub>	-0.3	0	-	V

### Note:

1. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambied as possible.

